



US 20150008394A1

(19) **United States**(12) **Patent Application Publication**
OH et al.(10) **Pub. No.: US 2015/0008394 A1**(43) **Pub. Date: Jan. 8, 2015**(54) **ORGANIC LIGHT EMITTING DIODE
DISPLAY**(22) Filed: **Dec. 5, 2013**(71) Applicant: **SAMSUNG DISPLAY CO., LTD.,
YONGIN-CITY (KR)**(30) **Foreign Application Priority Data**

Jul. 2, 2013 (KR) 10-2013-0077372

(72) Inventors: **Il-Soo OH**, Gyeonggi-Do (KR);
Chang-Ho Lee, Gyeonggi-Do (KR);
Ji-Hwan Yoon, Gyeonggi-Do (KR);
Dae-Yup Shin, Gyeonggi-Do (KR);
Hee-Joo Ko, Gyeonggi-Do (KR); **Se-Jin
Cho**, Gyeonggi-Do (KR); **Jin-Young
Yun**, Gyeonggi-Do (KR); **Bora Lee**,
Gyeonggi-Do (KR); **Yeon-Woo Lee**,
Gyeonggi-Do (KR); **Beom-Joon Kim**,
Gyeonggi-Do (KR); **Pyung-Eun Jeon**,
Gyeonggi-Do (KR); **Hyun-Ju Choi**,
Gyeonggi-Do (KR); **Joong-Won Sim**,
Gyeonggi-Do (KR); **In-Jae Lee**,
Gyeonggi-Do (KR)**Publication Classification**(51) **Int. Cl.**
H01L 27/32 (2006.01)
(52) **U.S. Cl.**
CPC **H01L 27/3244** (2013.01)
USPC **257/40**(73) Assignee: **SAMSUNG DISPLAY CO., LTD.,
YONGIN-CITY (KR)**(21) Appl. No.: **14/097,471**(57) **ABSTRACT**

An organic light emitting diode (OLED) display includes a substrate, a thin film transistor formed on the substrate, a first electrode formed on the thin film transistor and electrically connected to the thin film transistor, a hole injection layer (HIL) formed on the first electrode, a hole transport layer (HTL) formed on the hole injection layer (HIL), an emission layer formed on the HTL, an electron transport layer (ETL) formed on the emission layer, a first buffer layer located on the ETL, and a second electrode formed on the first buffer layer.

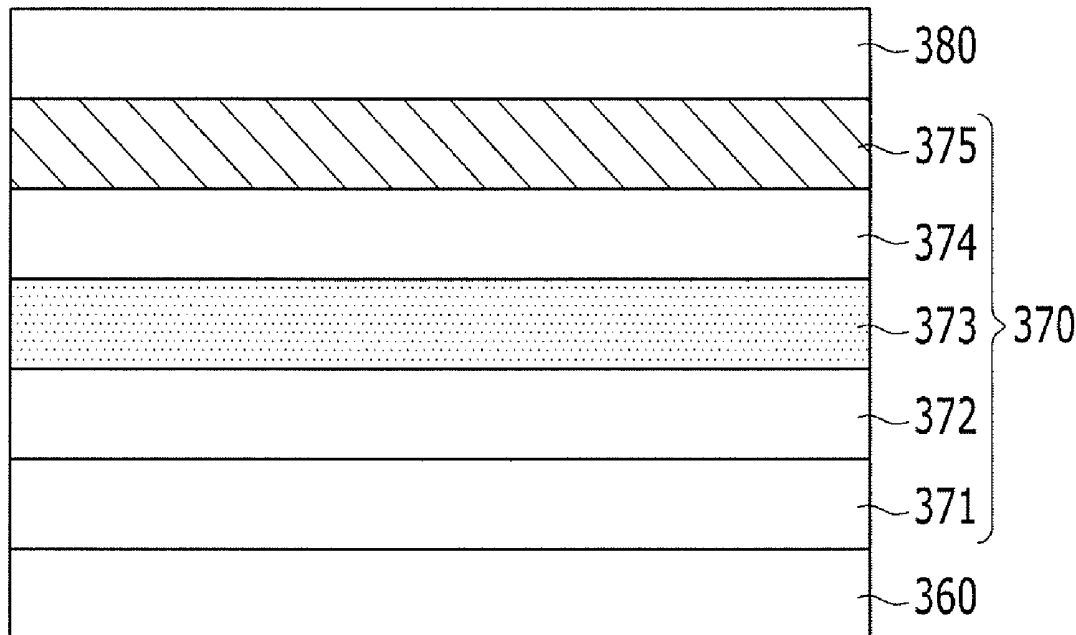


FIG. 2

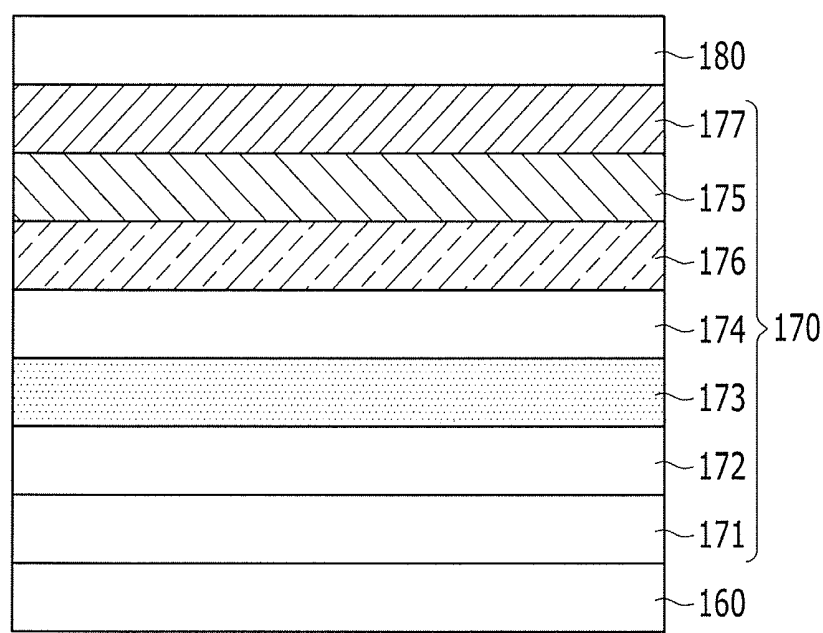


FIG. 3

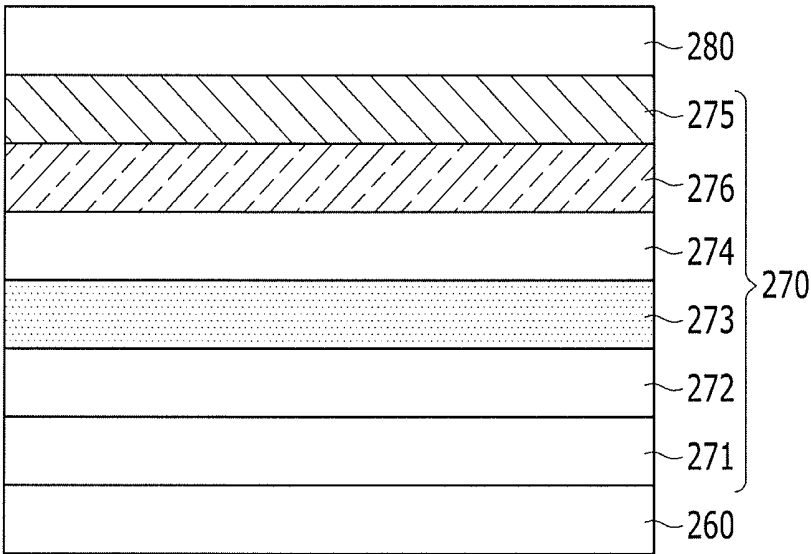


FIG. 4

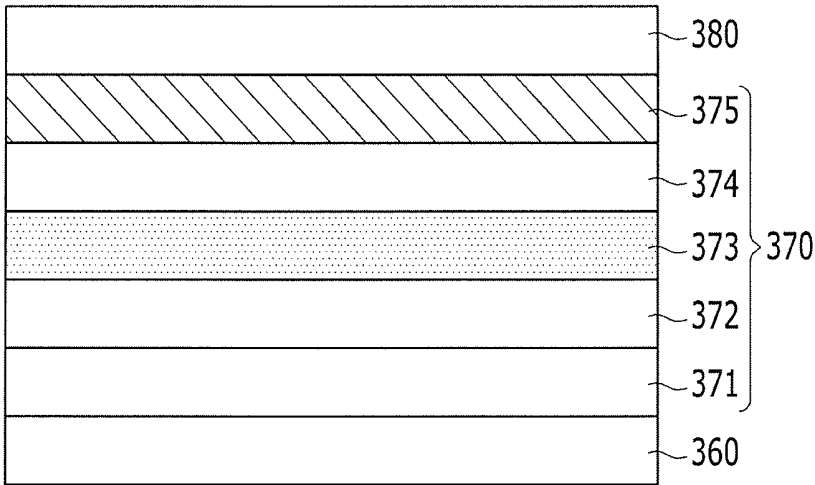


FIG. 5

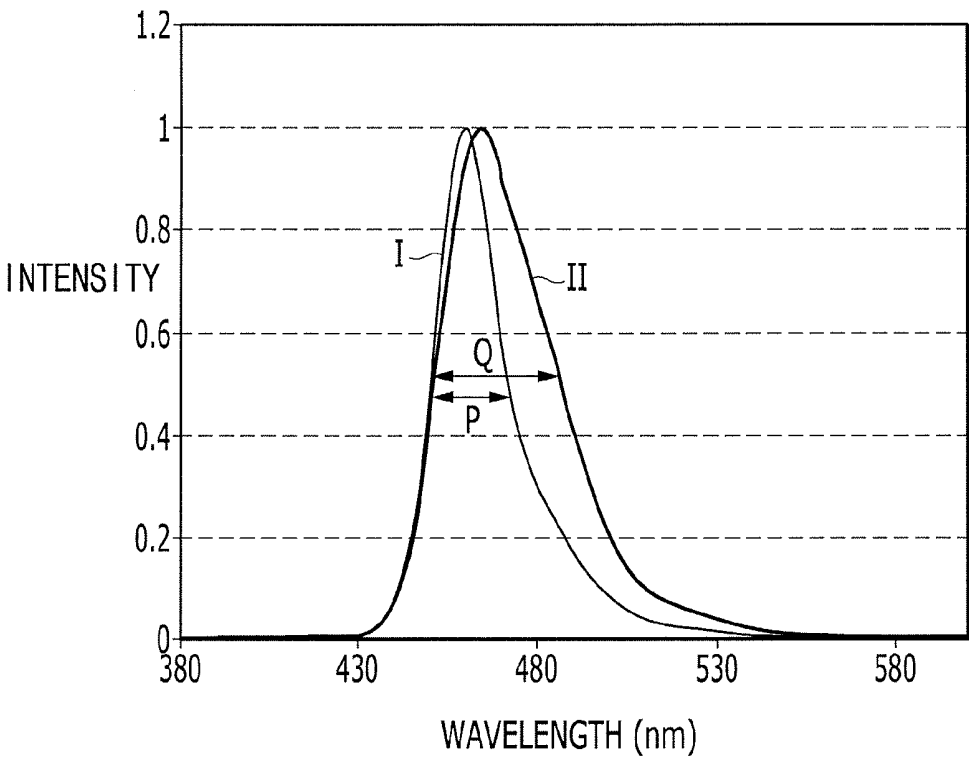
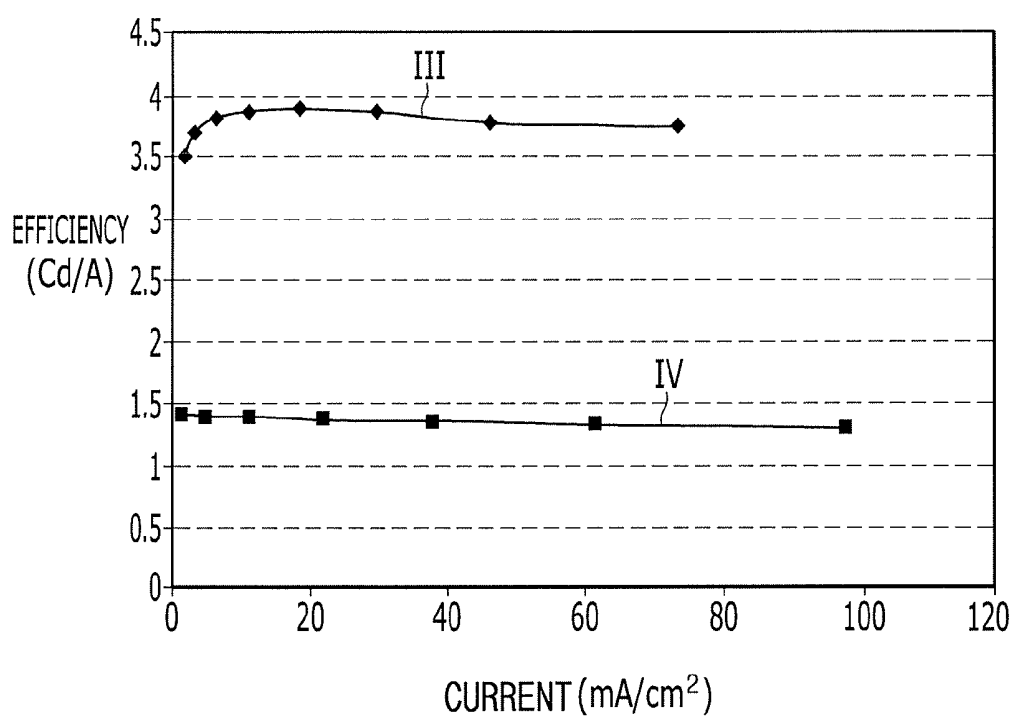


FIG. 6



ORGANIC LIGHT EMITTING DIODE DISPLAY

CROSS-REFERENCE TO RELATED APPLICATION

[0001] This application claims priority under 35 U.S.C. §119 to Korean Patent Application No. 10-2013-0077372, filed on Jul. 2, 2013 in the Korean Intellectual Property Office, the disclosure of which is incorporated by reference herein in its entirety.

TECHNICAL FIELD

[0002] The present invention relates to an organic light emitting diode (OLED) display.

DISCUSSION OF RELATED ART

[0003] Organic light emitting diode (OLED) displays are self-emissive using organic light emitting diodes. Holes and electrons are injected into the emission layer, and are recombined in the emission layer to generate excitons, thereby emitting light to display an image.

[0004] OLED displays include a pixel electrode and a common electrode to provide holes and electrons, respectively. The common electrode is formed using a sputtering process.

SUMMARY

[0005] An exemplary embodiment of the present invention provides an organic light emitting diode (OLED) display includes a substrate, a thin film transistor formed on the substrate, a first electrode formed on the thin film transistor and electrically connected to the thin film transistor, a hole injection layer (HIL) formed on the first electrode, a hole transport layer (HTL) formed on the hole injection layer (HIL), an emission layer formed on the HTL, an electron transport layer (ETL) formed on the emission layer, a first buffer layer located on the ETL, and a second electrode formed on the first buffer layer.

BRIEF DESCRIPTION OF THE DRAWINGS

[0006] These and other features of the present invention will become more apparent by describing in detail exemplary embodiments thereof with reference to the accompanying drawings of which:

[0007] FIG. 1 is a cross-sectional view illustrating an organic light emitting diode (OLED) display according to an exemplary embodiment of the present invention;

[0008] FIG. 2 is an enlarged cross-sectional view of an OLED display according to an exemplary embodiment of the present invention;

[0009] FIG. 3 is an enlarged cross-sectional view of an OLED display according to an exemplary embodiment of the present invention;

[0010] FIG. 4 is an enlarged cross-sectional view of an OLED display according to an exemplary embodiment of the present invention; and

[0011] FIG. 5 and FIG. 6 are graphs illustrating light intensity and luminous efficiency of the OLED display according to an exemplary embodiment of the present invention.

DETAILED DESCRIPTION OF THE EXEMPLARY EMBODIMENTS

[0012] Exemplary embodiments of the inventive concept will be described below in detail with reference to the accompanying drawings. However, the inventive concept may be embodied in different forms and should not be construed as limited to the embodiments set forth herein. In the drawings, the thickness of layers and regions may be exaggerated for clarity. It will also be understood that when an element is referred to as being "on" another element or substrate, it may be directly on the other element or substrate, or intervening layers may also be present. Like reference numerals may refer to the like elements throughout the specification and drawings.

[0013] Hereinafter, the configuration of an organic light emitting diode (OLED) display according to an exemplary embodiment will be described with reference to FIGS. 1 and 2. FIG. 1 is a cross-sectional view illustrating an organic light emitting diode (OLED) display according to an exemplary embodiment of the present invention. FIG. 2 is an enlarged cross-sectional view of an OLED display according to an exemplary embodiment of the present invention. The configuration of the OLED display may prevent damage to an organic light emitting element due to a sputtering process, and thus increase its luminous efficiency.

[0014] The OLED display includes a substrate 123, a thin film transistor 130, a first (pixel) electrode 160, a hole injection layer (HIL) 171, a hole transport layer (HTL) 172, an emission layer 173, an electron transport layer (ETL) 174, a first buffer layer 175, a second buffer layer 176, a third buffer layer 177, and a second (common) electrode 180.

[0015] The substrate 123 may be formed of an insulating substrate made of glass, quartz, ceramic, plastic, and the like. However, the present invention is not limited thereto, and the substrate 123 may also be formed as a metallic substrate made of stainless steel and the like.

[0016] A buffer layer 126 is formed on the substrate 123. The buffer layer 126 serves to prevent penetration of impurity elements and planarize the surface of the substrate 123. The buffer layer 126 may be formed of various materials including, but is not limited to, silicon nitride (SiN_x), silicon oxide (SiO_2), or silicon oxynitride (SiO_xN_y). Alternatively, the buffer layer 126 may be omitted according to the type of the substrate 123 and the condition of a process performed on the substrate 123.

[0017] A driving semiconductor layer 137 is formed on the buffer layer 126. The driving semiconductor layer 137 may be formed of a polysilicon film. The driving semiconductor layer 137 includes a channel region 135, and a source region 134 to and a drain region 136 which are doped and formed at opposite sides of the channel region 135. In this case, the doped impurities may be p-type impurities such as boron (B). For example, B_2H_6 may be used for doping boron into the source and drain regions 134 and 136. The doped impurities are not limited thereto, and may be various kinds of impurities according to the kind of a thin film transistor.

[0018] A gate insulating layer 127 may be formed of silicon nitride (SiN_x) or silicon oxide (SiO_2). The gate insulating layer 127 is formed on the driving semiconductor layer 137.

[0019] A driving gate electrode 133 is formed on the gate insulating layer 127. The driving gate electrode 133 overlaps at least the channel region 135 of the driving semiconductor layer 137.

[0020] An interlayer insulating layer 128 covers the driving gate electrode 133. The interlayer insulating layer 128 is formed on the gate insulating layer 127. Through holes H penetrates the interlayer insulating layer 128 and the gate insulating layer 127 to expose the source region 134 and the drain region 136. The interlayer insulating layer 128 may be formed of an insulating material such as silicon nitride (SiN_x), silicon oxide (SiO_2), or the like, similar to the gate insulating layer 127.

[0021] A data wire including a driving source electrode 131 and a driving drain electrode 132 is formed on the interlayer insulating layer 128. The driving source electrode 131 and the driving drain electrode 132 are respectively connected to the source region 134 and the drain region 136 of the driving semiconductor layer 137 via through holes H formed in the interlayer insulating layer 128 and the gate insulating layer 127.

[0022] The driving thin film transistor 130 including the driving semiconductor layer 137, the driving gate electrode 133, the driving source electrode 131, and the driving drain electrode 132 is formed. The configuration of the driving thin film transistor 130 is not limited thereto, and may be variously changed.

[0023] A planarization film 124 covering the data wire is formed on the interlayer insulating layer 128. The planarization layer 124 serves to planarize the interlayer insulating layer 128 to increase the light emitting efficiency of the organic light emitting element to be formed thereon. An electrode contact hole 122a penetrates the planarization layer 124 to expose a part of the drain electrode 132.

[0024] The planarization layer 124 may include, but is not limited to, polyacrylate resin, epoxy resin, phenolic resin, polyamide resin, polyimide resin, unsaturated polyesters resin, polyphenylenether resin, polyphenylene sulfide resin, or benzocyclobutene (BCB).

[0025] Alternatively, one of the planarization layer 124 and the interlayer insulating layer 128 may be omitted.

[0026] The pixel electrode 160 serving as a first electrode of the organic light emitting element is formed on the planarization layer 124. The OLED display includes a plurality of pixel electrodes 160. Each pixel electrode 160 is formed for a corresponding pixel of a plurality of pixels. Each pixel electrode 160 is spaced from each other. The pixel electrode 160 is connected to the drain electrode 132 through an electrode contact hole 122a.

[0027] A pixel defining layer 125 is formed on the planarization layer 124. The pixel defining layer 125 includes a plurality of openings. Each opening is formed for each pixel. An organic emission layer 170 is formed in the opening of the pixel defining layer 125. Accordingly, a pixel region including an organic emission layer is defined by the pixel defining layer 125.

[0028] The pixel defining layer 125 partially covers the pixel electrode 160 so that the opening of the pixel defining layer 125 exposes the pixel electrode. For example, the pixel electrode 160 is partially disposed beneath the pixel defining layer 125, and the pixel electrode 160 is partially overlapped by the pixel defining layer 125.

[0029] The pixel defining layer 125 may be formed of a resin such as polyacrylate resin, polyimide resin, or the like, or a silica-series inorganic material.

[0030] The organic emission layer 170 is formed on the pixel electrode 160. A detailed configuration of the organic emission layer 170 will be described hereinafter.

[0031] A common electrode 180 serving as a second electrode of the organic light emitting element is formed on the organic emission layer 170. As such, an organic light emitting element LD including the pixel electrode 160, the organic emission layer 170, and the common electrode 180 is formed.

[0032] The pixel electrode 160 and the common electrode 180 may be formed of a transparent conductive material, or a semitransparent or reflective conductive material. The OLED display may be a top emission type, a bottom emission type, or a both-side emission type according to the material of the pixel electrode 160 and the common electrode 180.

[0033] A cover film 190 covers the common electrode 180 to protect the common electrode 180. The cover film 190 may be formed of an organic film.

[0034] A thin film encapsulation layer 121 is formed on the cover layer 190. The thin film encapsulation layer 121 seals and protects the organic light emitting element LD and a driving circuit portion formed on the substrate 123 from the outside.

[0035] The thin film encapsulation layer 121 includes a plurality of encapsulation films 121a to 121d stacked on each other. The encapsulation films 121a and 121c may be formed of an organic material. The encapsulation films 121b and 121d may be formed of an inorganic material. The structure of the thin film encapsulation layer 121 is not limited thereto, but it may be layered in various ways.

[0036] FIG. 2 is an enlarged cross-sectional view of an organic light emitting element according to an exemplary embodiment of the present invention. FIG. 2 shows an enlarged view of an organic light emitting element LD of FIG. 1.

[0037] Referring to FIG. 2, the organic light emitting element LD (portion X of FIG. 1) includes the first electrode 160, the HIL 171, the HTL 172, the emission layer 173, the ETL 174, the second buffer layer 176, the first buffer layer 175, the third buffer layer 177, and the second electrode 180 that are stacked on each other.

[0038] The organic emission layer 170 of FIG. 1 includes the HIL 171, the HTL 172, the emission layer 173, the ETL 174, the second buffer layer 176, the first buffer layer 175, and the third buffer layer 177.

[0039] The HIL 171 is disposed on the first electrode 160. The HIL 171 serves to inject holes from the first electrode 160 into the HTL 172. The HIL 171 may be formed of, but is not limited to, copper phthalocyanine (CuPc), poly(3,4-ethylenedioxythiophene) (PEDOT), polyaniline (PANI), or N,N'-diphenyl-N,N'-di-[4-(N,N-diphenyl-amino)phenyl]benzidine (NPNPB).

[0040] The thickness of the HIL 171 may range from about 25 nm to about 35 nm. For example, the thickness of the HIL 171 may be about 30 nm.

[0041] The HTL 172 is disposed on the HIL 172. The HTL 172 serves to transport the holes from the HIL 171 to the emission layer 173. For example, the HTL 172 may be formed of, but is not limited to, N,N-di(1-naphthyl)-N,N'-di(phenyl)benzidine (NPD), N,N'-bis(3-methylphenyl)-N,N'-bis(phenyl) (TPD), s-TAD, or 4,4',4''-tris(N-3-methylphenyl-N-phenyl-amino)-triphenylamine (MTDATA).

[0042] The thickness of the HTL 172 may range about 15 nm to about 25 nm. For example, the thickness of the HTL 172 may be about 20 nm.

[0043] The emission layer 173 contains a light emitting material for displaying a predetermined color. For example,

the emission layer **173** may display a primary color such as blue, green, or red, or a combination thereof.

[0044] The thickness of the emission layer **173** may range from about 15 nm to about 25 nm. For example, the thickness of the emission layer **173** may be about 20 nm.

[0045] The emission layer **173** contains a host material and a dopant. The emission layer **173** may contain materials for emitting red, green, blue, and white light, and may be formed by using a phosphorescent or fluorescent material.

[0046] For red light, the emission layer **173** contains a host material having carbazole biphenyl (CBP) or 1,3-bis(carbazol-9-yl) (mCP), and may be formed of a phosphorescent material containing a dopant including, but is not limited to, PIQIr(acac) (bis(1-phenylisoquinoline)acetylacetonate iridium), PQIr(acac) (bis(1-phenylquinoline)acetylacetonate iridium), PQIr (tris(1-phenylquinoline)iridium), or PtOEP (platinum octaethylporphyrin).

[0047] For green light, the emission layer **173** contains a host material having CBP or mCP, and may be formed of a phosphorescent material containing a dopant including Ir(ppy)₃ (fac-tris(2-phenylpyridine)iridium) or a phosphorescent material containing a dopant having Alq₃ (tris(8-hydroxyquinolino)aluminum).

[0048] For blue light, the emission layer **173** contains a host material having CBP or mCP, and may be formed of a phosphorescent material containing a dopant (4,6-F2ppy)₂Irpic. Alternatively, the emission layer **173** may be formed of a phosphorescent material including, but is not limited to, spiro-DPVBi, spiro-6P, distyryl benzene (DSB), distyryl arylene (DSA), a PFO-based polymer, or a PPV-based polymer.

[0049] The ETL **174** is disposed on the emission layer **173**. The ETL **174** serves to transfer electrons from the second electrode **180** to the emission layer **173**. Further, the ETL **174** serves to prevent the holes injected from the first electrode **160** from moving to the second electrode **180** through the emission layer **173**. For example, the ETL **174** serves as a hole blocking layer to improve combination of the holes and electrons in the emission layer **173**.

[0050] The ETL **174** may be formed of, but is not limited to, Alq₃ (tris(8-hydroxyquinolino)aluminum), PBD, TAZ, spiro-PBD, BAlq, or SAIq.

[0051] The second buffer layer **176** is disposed on the electron transport layer (ETL) **174**.

[0052] The second buffer layer **176** serves to inject electrons from the first buffer layer **175** into the ETL **174**. The second buffer layer **176** may be formed of, but is not limited to, LiF, Yb, WO₃, MoO_x, Liq, or Mg. Herein, the MoO_x indicates a molybdenum oxide.

[0053] The thickness of the second buffer layer **176** may range from about 1 nm to about 3 nm. For example, the thickness of the second buffer layer **176** may be about 2 nm.

[0054] The first buffer layer **175** is disposed on the second buffer layer **176**. The second buffer layer **176** is disposed between the first buffer layer **175** and the ETL **174**.

[0055] The first buffer layer **175** facilitates smooth movement of the electrons transferred from the second electrode **180** to a layer having higher electron mobility. The first buffer layer **175** may be formed of, but is not limited to, copper phthalocyanine (CuPc), copper hexadecafluorophthalocyanine (F₁₆CuPc), molybdenum oxide (MoO_x), tungsten trioxide (WO₃), or HAT-CN. Alternatively, the first buffer layer **175** may be formed of a polycrystalline organic or inorganic material having high electron mobility.

[0056] The thickness of the first buffer layer **175** may range from about 85 nm to about 95 nm. For example, the thickness of the first buffer layer **175** may be about 90 nm to increase the luminous efficiency of the OLED display. For example, the luminous efficiency of the OLED display may be maximized at such thickness. The luminous efficiency of the OLED display according to an exemplary embodiment will be described hereinafter.

[0057] The first buffer layer **175** serves to prevent damage to the emission layer **173** from occurring from a sputtering process for forming the common electrode **180**.

[0058] The third buffer layer **177** is disposed between the first buffer layer **175** and the second electrode **180**.

[0059] The third buffer layer **177** serves to inject electrons from the second electrode **180** into the first buffer layer **450**. The third buffer layer **177** may be formed of, but is not limited to, lithium fluoride (LiF), Yb, WO₃, MoO_x, Liq, or Mg. Herein, the MoO_x indicates various molybdenum oxides.

[0060] The thickness of the third buffer layer **177** may range from about 1 nm to about 3 nm. For example, the thickness of the third buffer layer **177** may be about 2 nm.

[0061] FIG. 3 is an enlarged cross-sectional view of an organic light emitting element according to an exemplary embodiment of the present invention. FIG. 3 shows an enlarged view of an organic light emitting element LD (portion X) of FIG. 1. The organic light emitting element LD of FIG. 3 is substantially similar to that of FIG. 2, except that the third buffer layer **177** of FIG. 2 is omitted. Hereinafter, differences will be described in detail, and the description of the same elements will be omitted.

[0062] Referring to FIG. 3, the organic light emitting element LD of FIG. 3 includes a second buffer layer **276**, a first buffer layer **275**, and a second electrode **280** that are sequentially stacked on an ETL **274**.

[0063] The second buffer layer **276** serves to inject electrons from the first buffer layer **275** into the ETL **274**. The second buffer layer **276** may be formed of, but is not limited to, LiF, Yb, WO₃, MoO_x, Liq, or Mg. Herein, the MoO_x indicates a molybdenum oxide.

[0064] The thickness of the second buffer layer **276** may range from about 1 nm to about 3 nm. For example, the thickness of the second buffer layer **276** may be about 2 nm.

[0065] The first buffer layer **275** facilitates smooth movement of the electrons transferred from the second electrode **280** to a layer having higher electron mobility. The first buffer layer **275** may be formed of, but is not limited, copper phthalocyanine (CuPc), copper hexadecafluorophthalocyanine (F₁₆CuPc), molybdenum oxide (MoO_x), tungsten trioxide (WO₃), or HAT-CN. Alternatively, the first buffer layer **275** may be formed of a polycrystalline organic or inorganic material having high electron mobility.

[0066] The thickness of the first buffer layer **275** may range from about 85 nm to about 95 nm. For example, the thickness of the first buffer layer **275** may be about 90 nm.

[0067] FIG. 4 is an enlarged cross-sectional view of an organic light emitting element according to an exemplary embodiment of the present invention. FIG. 4 shows an enlarged view of an organic light emitting element LD (portion X) of FIG. 1. The organic light emitting element LD of FIG. 4 is substantially similar to that of FIG. 2, except that the second and third buffer layers **176** and **177** of FIG. 2 are omitted. Hereinafter, differences will be described in detail, and the description of the same elements will be omitted.

[0068] Referring to FIG. 4, the organic light emitting element LD includes a first buffer layer 375 and a second electrode 380 that are sequentially stacked on an ETL 374.

[0069] The first buffer layer 375 facilitates smooth movement of the electrons, transferred from the second electrode 380, to a layer having higher electron mobility. The first buffer layer 375 may be formed of, but is not limited to, copper phthalocyanine (CuPc), copper hexadecafluorophthalocyanine ($F_{16}CuPc$), molybdenum oxide (MoO_x), tungsten trioxide (WO_3), or HAT-CN. Alternatively, the first buffer layer 375 may be formed of a polycrystalline organic or inorganic material having high electron mobility.

[0070] The thickness of the first buffer layer 375 may range from about 85 nm to about 95 nm. For example, the thickness of the first buffer layer 375 may be about 90 nm.

[0071] FIG. 5 and FIG. 6 are graphs illustrating light intensity and luminous efficiency of the OLED display according to an exemplary embodiment of the present invention.

[0072] The measuring results I and III represent light intensity and luminous efficiency, respectively, of an experimental sample according to an exemplary embodiment. The experimental sample includes an organic light emitting element, where the HIL has a thickness of about 30 nm, the HTL has a thickness of about 20 nm, the emission layer has a thickness of about 20 nm, the ETL has a thickness of 30 nm, the second buffer layer has a thickness of 2 nm, the first buffer layer has a thickness of about 90 nm, and the third buffer layer has a thickness of 2 nm. The first buffer layer, the second buffer layer, and the third buffer layer are respectively made of Yb+LiF, MoO_x , and Yb+LiF. The HIL, the HTL, the emission layer, the ETL are respectively made of DNTPD, NPB, (MADN, BCzVBi), and Alq3.

[0073] The measuring results II and IV represents light intensity and luminous efficiency of a comparative sample of a conventional OLED display in which the HIL has a thickness of about 10 nm, the HTL has a thickness of about 130 nm, the emission layer has a thickness of about 20 nm, the ETL has a thickness of about 30 nm, and the electron injection layer (EIL) has a thickness of 1 nm. The HIL, the HTL, the emission layer, the ETL, and the EIL are respectively made of DNTPD, NPB, (MADN, BCzVBi), Alq3, and LiF.

[0074] Referring to FIG. 5, the measuring result I for the experimental sample has a maximum intensity at the wavelength of 460 nm and a width P of the maximum wavelength (460 nm) (at an intensity 0.5). The measuring result II for the comparative sample has a maximum intensity at the wavelength of 480 nm and has a width Q (at an intensity 0.5). The width P is smaller than the width Q. Accordingly, the experimental sample emits light whose wavelengths are more concentrated at a specific wavelength of a specific color than the comparative sample. For example, the OLED display according to an exemplary embodiment emits light having a specific wavelength. As a result, the OLED display has an increased image quality.

[0075] Referring to FIG. 6, the luminous efficiency III for the experimental sample has higher than the luminous efficiency IV for the comparative sample.

[0076] According to an exemplary embodiment, an OLED display has significantly higher luminous efficiency than that of the conventional OLED display, and may prevent damage to an organic light emitting element from occurring due to a sputtering process using multi-layered buffer layers located on an ETL.

[0077] While the present inventive concept has been shown and described with reference to exemplary embodiments thereof, it will be apparent to those of ordinary skill in the art that various changes in form and detail may be made therein without departing from the spirit and scope of the inventive concept as defined by the following claims.

What is claimed is:

1. An organic light emitting diode (OLED) display comprising:

- a substrate;
- a thin film transistor formed on the substrate;
- a first electrode formed on the thin film transistor and electrically connected to the thin film transistor;
- a hole injection layer (HIL) formed on the first electrode;
- a hole transport layer (HTL) formed on the hole injection layer (HIL);
- an emission layer formed on the HTL;
- an electron transport layer (ETL) formed on the emission layer;
- a first buffer layer located on the ETL; and
- a second electrode formed on the first buffer layer.

2. The OLED display of claim 1, further comprising a second buffer layer formed between the ETL and the first buffer layer.

3. The OLED display of claim 2, further comprising a third buffer layer formed between the first buffer layer and the second electrode.

4. The OLED display of claim 1, wherein the first buffer layer comprises CuPc, $F_{16}CuPc$, MoO_x , or WO_3 .

5. The OLED display of claim 2, wherein the second buffer layer comprises LiF, Yb, WO_3 , MoO_x , Liq, or Mg.

6. The OLED display of claim 3, wherein the third buffer layer comprises LiF, Yb, WO_3 , MoO_x , Liq, or Mg.

7. The OLED display of claim 1, wherein a thickness of the emission layer ranges from about 15 nm to about 25 nm.

8. The OLED display of claim 7, wherein a thickness of the HIL ranges from about 25 nm to about 35 nm, and a thickness of the HTL ranges from about 15 nm to about 25 nm.

9. The OLED display of claim 1, wherein a thickness of the first buffer layer ranges from about 85 nm to about 95 nm.

10. The OLED display of claim 2, wherein a thickness of the second buffer layer ranges from about 1 nm to about 3 nm.

11. The OLED display of claim 3, wherein a thickness of the third buffer layer ranges from about 1 nm to about 3 nm.

12. An organic light emitting diode (OLED) display comprising:

- a substrate;
- a thin film transistor formed on the substrate;
- a first electrode formed on the thin film transistor and electrically connected to the thin film transistor;
- a hole injection layer (HIL) formed on the first electrode;
- a hole transport layer (HTL) formed on the hole injection layer (HIL);
- an emission layer formed on the HTL;
- an electron transport layer (ETL) formed on the emission layer;
- a first buffer layer located on the ETL, wherein the first buffer layer is thicker than the HIL; and
- a second electrode formed on the first buffer layer.

* * * * *

专利名称(译)	有机发光二极管显示器		
公开(公告)号	US20150008394A1	公开(公告)日	2015-01-08
申请号	US14/097471	申请日	2013-12-05
[标]申请(专利权)人(译)	三星显示有限公司		
申请(专利权)人(译)	三星DISPLAY CO. , LTD.		
当前申请(专利权)人(译)	三星DISPLAY CO. , LTD.		
[标]发明人	OH IL SOO LEE CHANG HO YOON JI HWAN SHIN DAE YUP KO HEE JOO CHO SE JIN YUN JIN YOUNG LEE BORA LEE YEON WOO KIM BEOM JOON JEON PYUNG EUN CHOI HYUN JU SIM JOONG WON LEE IN JAE		
发明人	OH, IL-SOO LEE, CHANG-HO YOON, JI-HWAN SHIN, DAE-YUP KO, HEE-JOO CHO, SE-JIN YUN, JIN-YOUNG LEE, BORA LEE, YEON-WOO KIM, BEOM-JOON JEON, PYUNG-EUN CHOI, HYUN-JU SIM, JOONG-WON LEE, IN-JAE		
IPC分类号	H01L27/32		
CPC分类号	H01L27/3244 H01L51/5092 H01L2251/558		
优先权	1020130077372 2013-07-02 KR		
外部链接	Espacenet USPTO		

摘要(译)

有机发光二极管 (OLED) 显示器包括基板, 形成在基板上的薄膜晶体管, 形成在薄膜晶体管上并电连接到薄膜晶体管的第一电极, 形成在薄膜晶体管上的空穴注入层 (HIL) 第一电极, 在空穴注入层 (HIL) 上形成的空穴传输层 (HTL), 在HTL上形成的发光层, 在发光层上形成的电子传输层 (ETL), 位于ETL上的第一缓冲层, 以及在第一缓冲层上形成的第二电极。

